

Mrs.Padmavati S. Pise
Associate Professor
padmavatipise@gmail.com

➤ **Educational Qualification: M.Tech. (Electronics Technology), B.E.(Electronics Engg.)**

➤ **Experience: 27 years**

➤ **Contact No. (optional) :**

➤ **Areas of Interest: VLSI Design , Embedded Systems**

➤ **Memberships: ISTE (LM-18341)**

➤ **Courses Handled: UG Level:**

1. VLSI Design
2. Network Analysis
3. Digital Systems & Microprocessor
4. Electronic System Design
5. Power Electronics
6. Power Electronics and Drives
7. System On Chip
8. Electronic Circuit Analysis & design-I
9. Electronic Measurement & Instrumentation
10. Microcontroller
11. Microprocessor

➤ **Administrative responsibilities:**

Institute Level:

- Presiding Officer of Internal Complaints Committee (Jan 2016 onwards)
- Coordinator of Robotics Club (June 2018 onwards)
- Member of Anti ragging Committee
- Member of Examination Committee
- Member of Central Computing Facility Committee
- Member Time table Committee

➤ **Publications:**

International Journals and E-Letters: **3**

International Conferences: **4 (1 Best paper award)**

National Conferences: **2**

➤ **Patents: Nil**

➤ **Research Projects :**

➤ **Awards and Accolades:**

1. Best Paper Award (ICETETA 2017)